Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3	(("6112252") or ("5872920")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2003/06/18 07:07
S2	1	"5210749".PN.	USPAT	OR	OFF	2002/03/28 07:32
S3	1	"5210749".PN.	USPAT	OR	OFF	2002/04/05 11:12
S4	27	("4258418" | "4590467" | "4715030" | "4754399" | "4768190" | "4852088" | "4860193" | "4907225" | "5043981" | "5101477" | "5119374" | "5121479" | "5133062" | "5133078" | "5136582" | "5195093" | "5210749" | "5210829" | "5235595" | "5247626" | "5278956" | "5289579" | "5293487" | "5329622" | "5412780" | "5412782" | "5487152").PN.	USPAT	OR	OFF	2002/03/28 07:32
S5	12162	MAC	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2003/01/27 08:33
S6	226	709/250 and MAC	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2003/01/19 14:52
S7	19	(709/250 and MAC) and register and interrupt and PHY	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2002/03/28 15:11
S8	1049	network near4 protocol near3 snmp	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2002/03/28 15:12
S9	31970	encoding and decoding	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2002/03/28 15:12
S10	61	(network near4 protocol near3 snmp) and (encoding and decoding)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2002/03/30 16:17
S11	880	MAC and buffer and register? and circuitry	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 14:11
S12	0	(MAC and buffer and register? and circuitry) and (interrupt adj bite)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 06:35
S13	558	(MAC and buffer and register? and circuitry) and interrupt	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 06:36
S14	455	((MAC and buffer and register? and circuitry) and interrupt) and byte	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 06:36

S15	297	(((((MAC and buffer and register? and circuitry) and interrupt) and byte) and host) and data) and circuitry	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 06:37
S16	130	((((((MAC and buffer and register? and circuitry) and interrupt) and byte) and host) and data) and circuitry) and latch and error and read and write	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 06:39
S17	128	(((((((MAC and buffer and register? and circuitry) and interrupt) and byte) and host) and data) and circuitry) and latch and error and read and write) and controller	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 07:00
S18	18	((((((((MAC and buffer and register? and circuitry) and interrupt) and byte) and host) and data) and circuitry) and latch and error and read and write) and csma/cd) and csma/cd	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 07:06
S19	1137	ethernet near3 controller	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 07:07
S20	48	(((((((MAC and buffer and register? and circuitry) and interrupt) and byte) and host) and data) and circuitry) and latch and error and read and write) and (ethernet near3 controller)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2002/03/29 07:07
S21	1	("6065073").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2002/03/30 16:17
S22	4	("4451916" | "4761762" | "5561767" | "5566319").PN.	USPAT	OR	OFF	2002/03/30 16:18
S23	2	("5485584").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2002/04/02 07:47
S24	2	("5764896").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2002/04/05 11:20
S25	4	("5297139" | "5307459" | "5434872" | "5606559").PN.	USPAT	OR	OFF	2003/01/18 14:52
S26	1	("6154796").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2003/01/18 14:53

S27	321	709/250 and MAC	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/19 14:52
S28	4	(("5764896") or ("6047001") or ("6154796") or ("5458584")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2003/01/19 14:53
S29	8	(709/250 and MAC) and (transmit adj buffer) and (receive adj buffer) and client and network and FIFO and collision	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/19 14:58
S30	1453	network adj interface adj adapter	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/19 14:58
S31	15	(network adj interface adj adapter) and MAC and 709/250	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/19 14:59
S32	0	MAC with (data adj registers) and (transmit adj buffer) and network and (format$3 near3 data)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 13:52
S33	0	(MAC with (data adj registers)) and (transmit adj buffer) and network	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 13:51
S34	0	(MAC with (data adj registers)) and (transmit adj buffer)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 13:51
S35	46	MAC with (data adj registers)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 13:51
S36	0	(MAC with (data adj registers)) and (transmit adj buffer) and (format$3 near3 data)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 13:52
S37	9	(MAC with (data adj registers)) and (format$3 near3 data)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 13:52
S38	1188	MAC and buffer and register? and circuitry	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 14:12
S39	20	(MAC and buffer and register? and circuitry) and 709/250 and PHY and monitor and format and write	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/26 14:13
S40	34	(MAC adj chip) and PHY	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/06/17 15:20
S41	16	((MAC adj chip) and PHY) and format and write and monitor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 09:30

S42	16	((MAC adj chip) and PHY) and format and write and monitor and transmit and receive	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 09:09
S43	16	(((MAC adj chip) and PHY) and format and write and monitor and transmit and receive) and collision	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 09:31
S44	16	((((MAC adj chip) and PHY) and format and write and monitor and transmit and receive) and collision) and condition	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 09:33
S45	16	(((((MAC adj chip) and PHY) and format and write and monitor and transmit and receive) and collision) and condition) and caus$5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 09:40
S46	14	((((((MAC adj chip) and PHY) and format and write and monitor and transmit and receive) and collision) and condition) and caus$5) and "802.3"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 09:39
S47	16	(((((MAC adj chip) and PHY) and format and write and monitor and transmit and receive) and collision) and condition) and caus$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 09:43
S48	16	((((((MAC adj chip) and PHY) and format and write and monitor and transmit and receive) and collision) and condition) and caus$5) and caus$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 09:41
S49	16	(((((MAC adj chip) and PHY) and format and write and monitor and transmit and receive) and collision) and condition) and cause	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 12:39
S50	16	(((((MAC adj chip) and PHY) and format and write and monitor and transmit and receive) and collision) and condition) and cause and register	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 12:40
S51	34	(MAC adj chip) and PHY	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/06/18 07:10
S52	27	((MAC adj chip) and PHY) and "802.3"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 12:45
S53	14	(((MAC adj chip) and PHY) and "802.3") and register and write and read and monitor and buffer and format and transmit and receive	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/27 12:46
S54	1429	709/250	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/29 07:47

S55	4814	707/1	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/29 07:47
S56	1103	370/463	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/29 07:47
S57	4144	370/401	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/29 07:48
S58	799	370/419	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/29 07:48
S59	2337	709/201	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/01/29 07:48
S60	2	(("6154796") or ("5458584")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2003/06/17 15:22
S61	2	(("6154796") or ("5485584")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2003/06/17 15:22
S62	3	(("6112252") or ("5872920") or ("6345310")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2003/06/18 07:07
S63	18	(MAC adj chip) and PHY and interrupt	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/06/18 11:26
S64	3	(MAC adj chip) and PHY and interrupt and status and error and instruction	US-PGPUB; USPAT; EPO; JPO	OR	ON	2003/06/18 11:27